

1/9

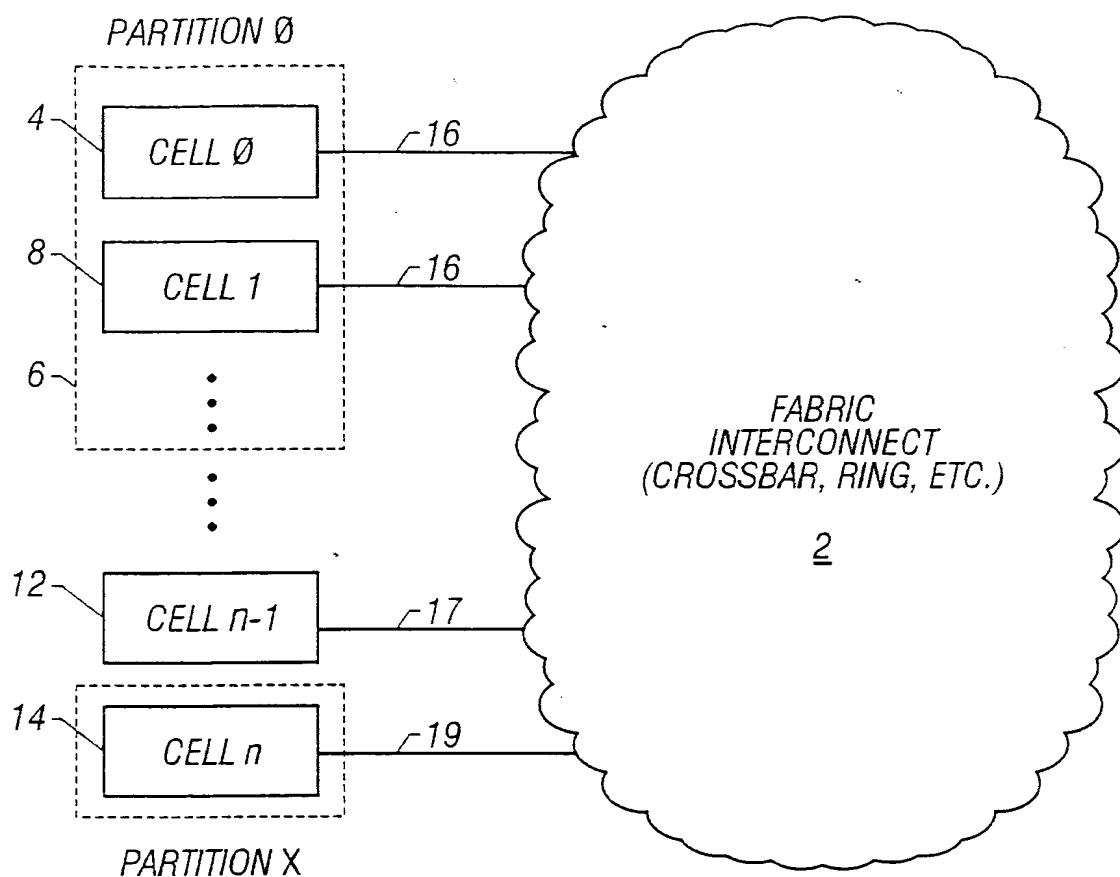


FIG. 1

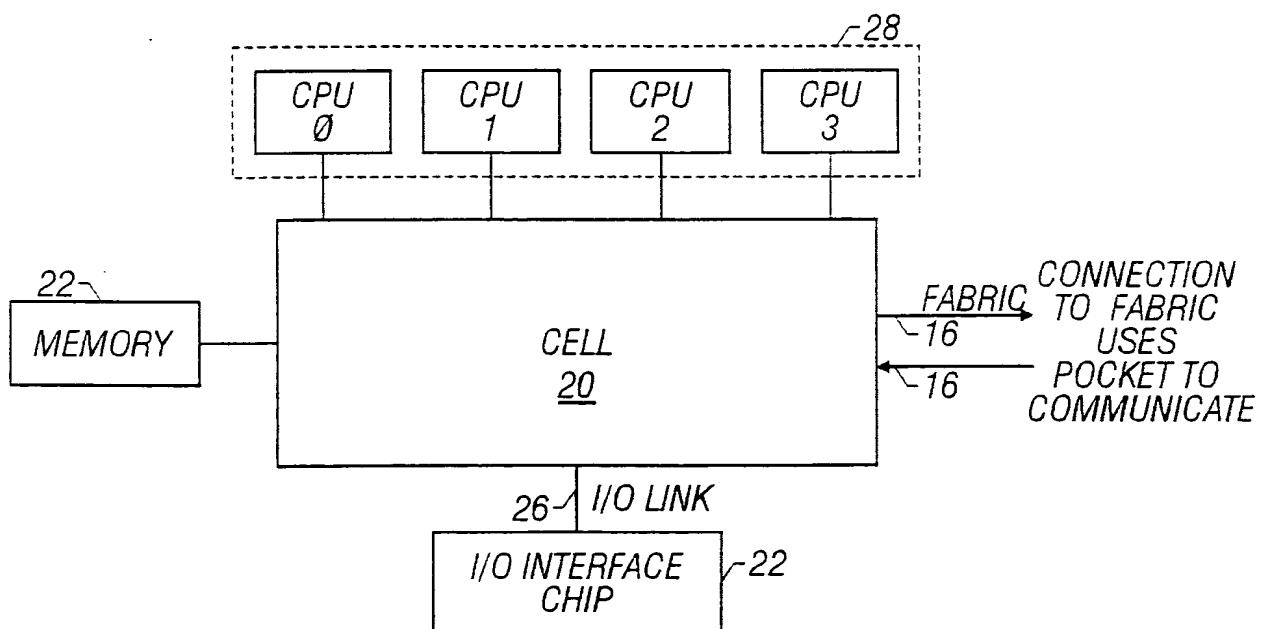
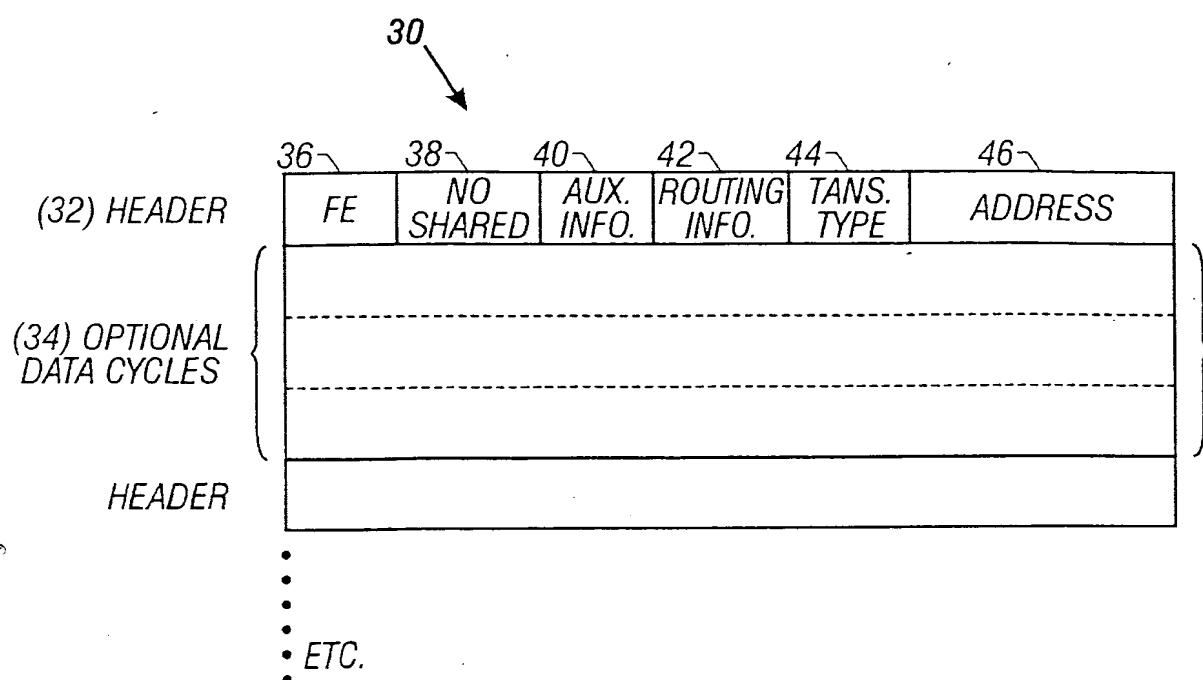


FIG. 2

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**FIG. 3**

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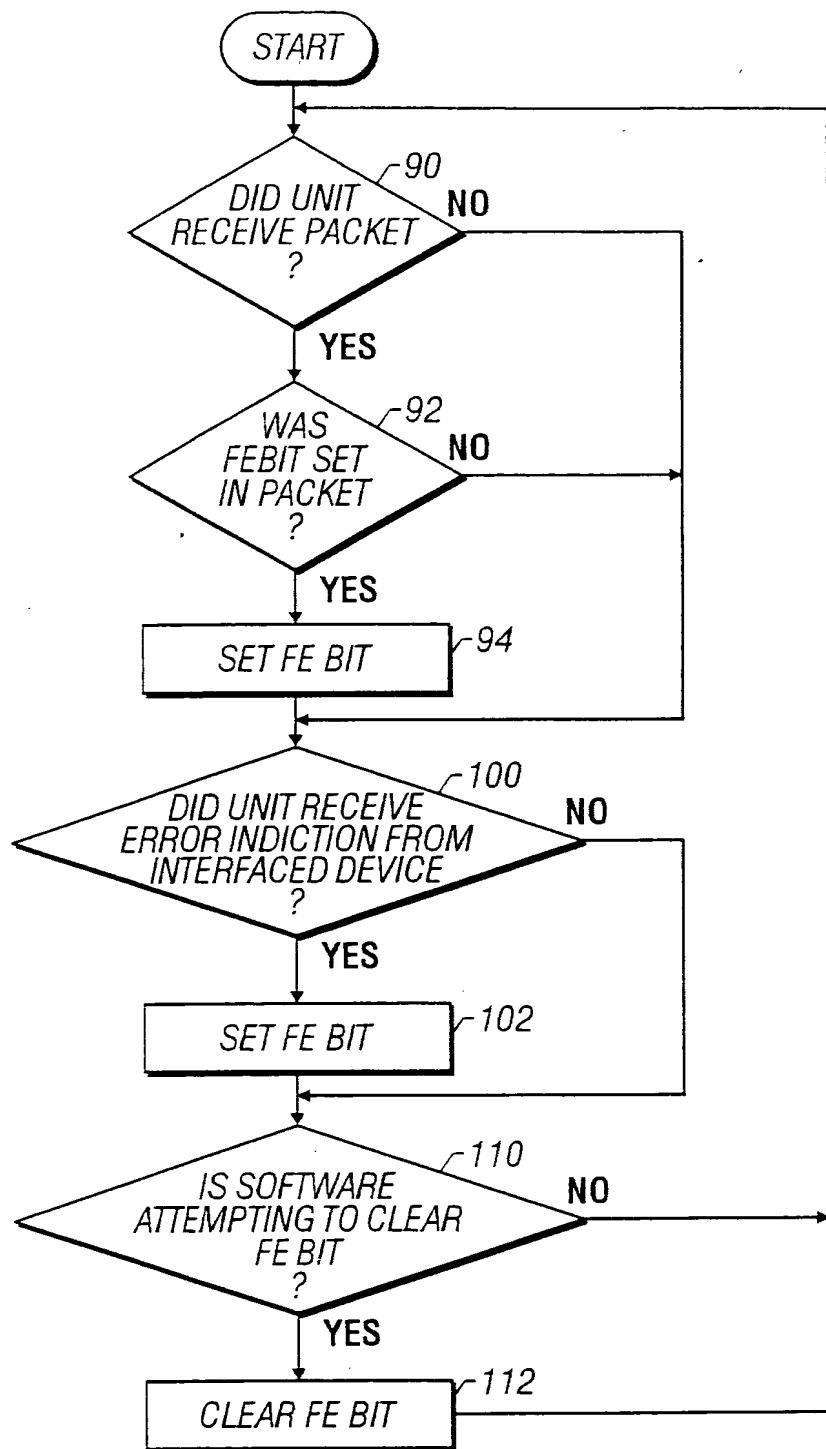


FIG. 4

METHOD FOR ALLOWING DISTRIBUTED HIGH  
PERFORMANCE COHERENT MEMORY WITH FULL ERROR  
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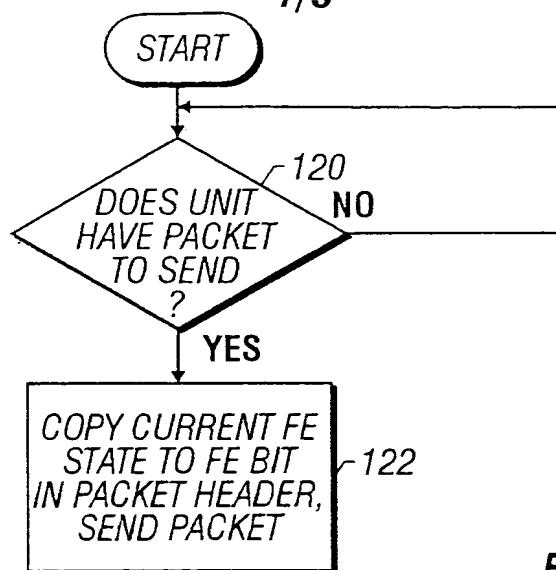


FIG. 5

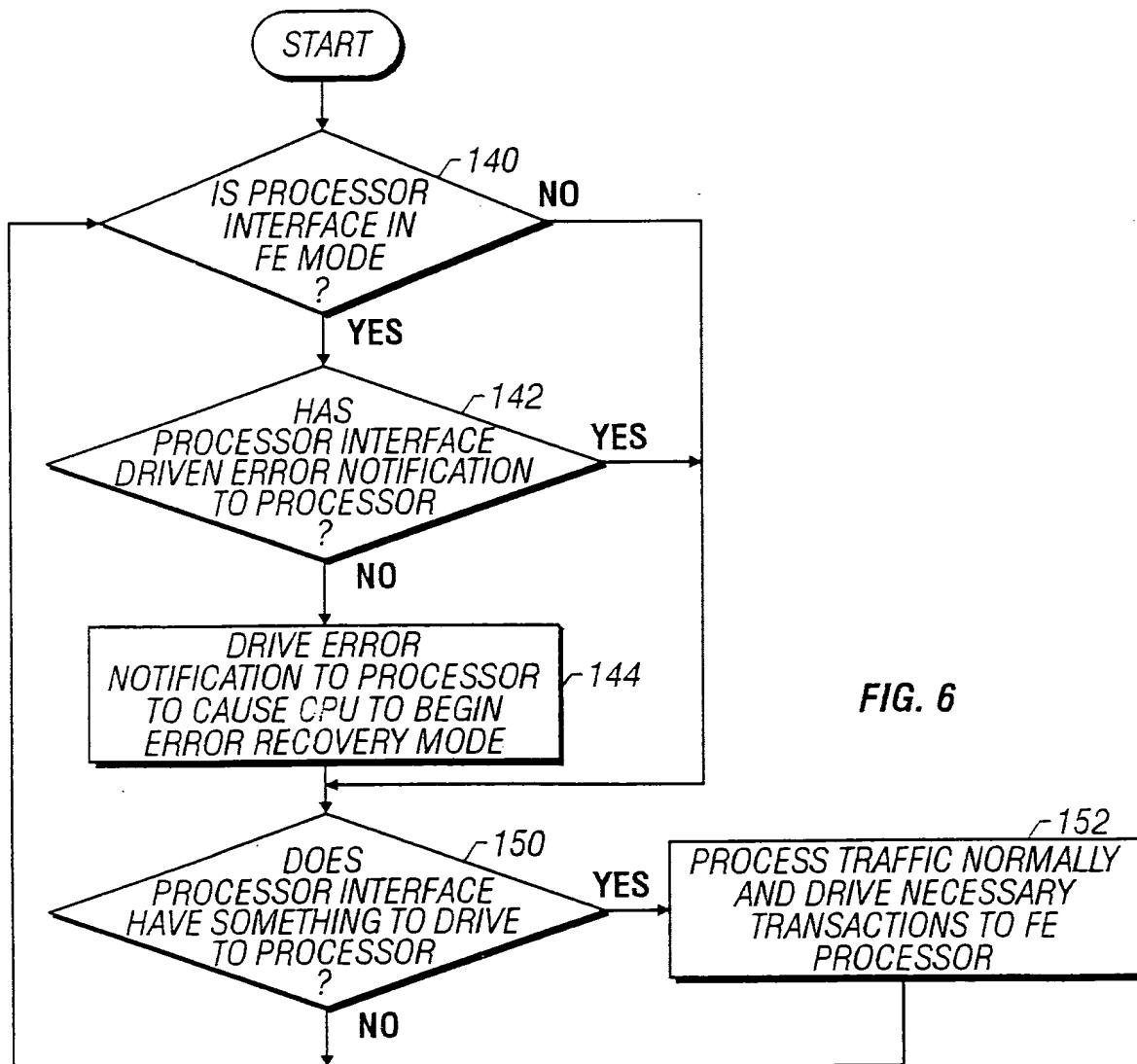


FIG. 6

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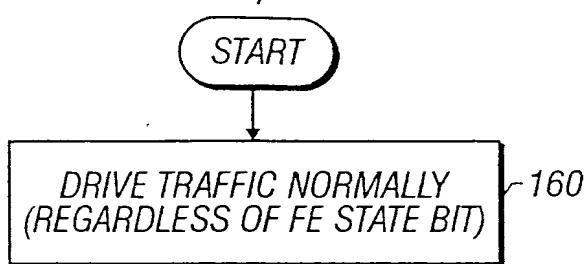


FIG. 7

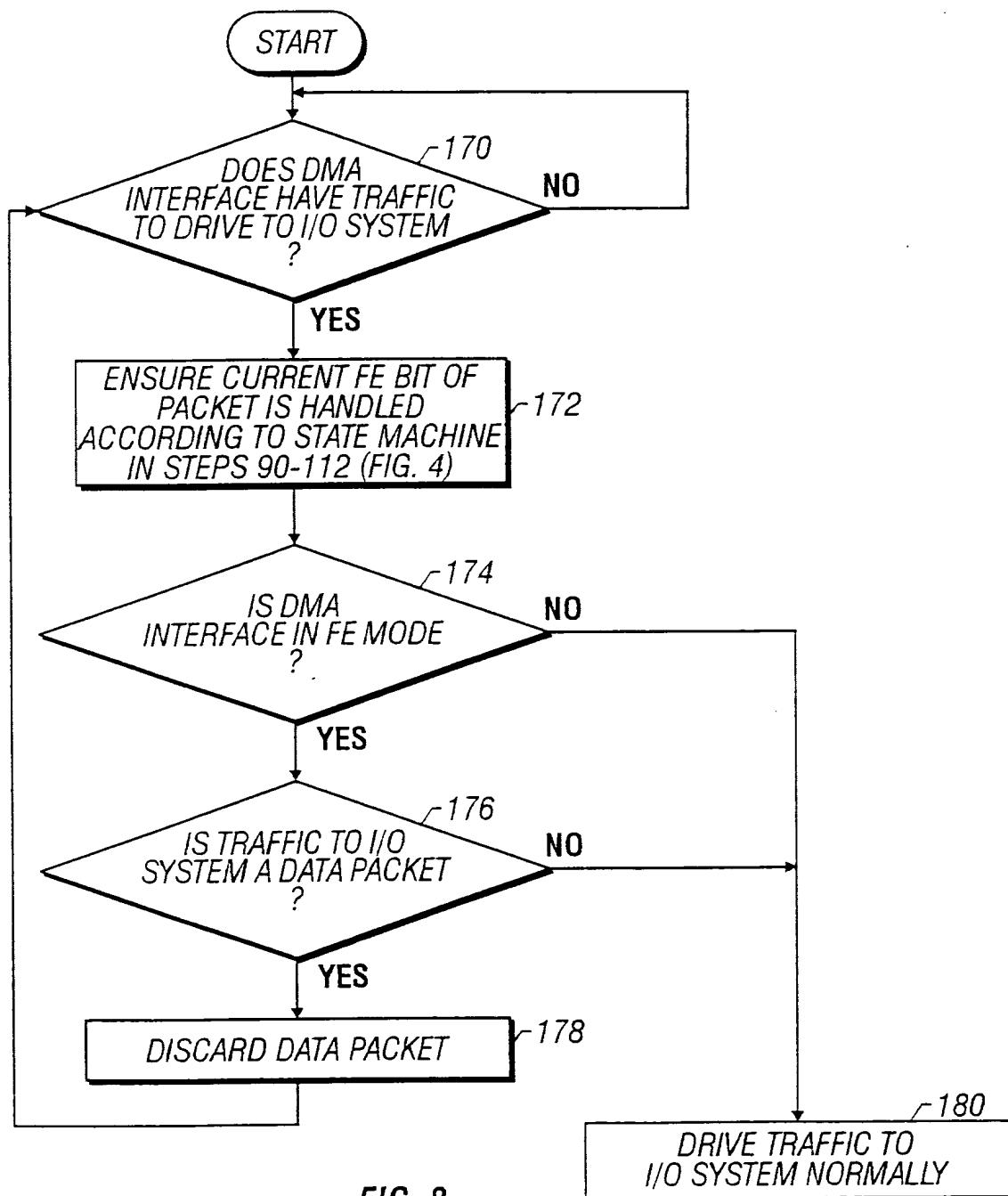


FIG. 8

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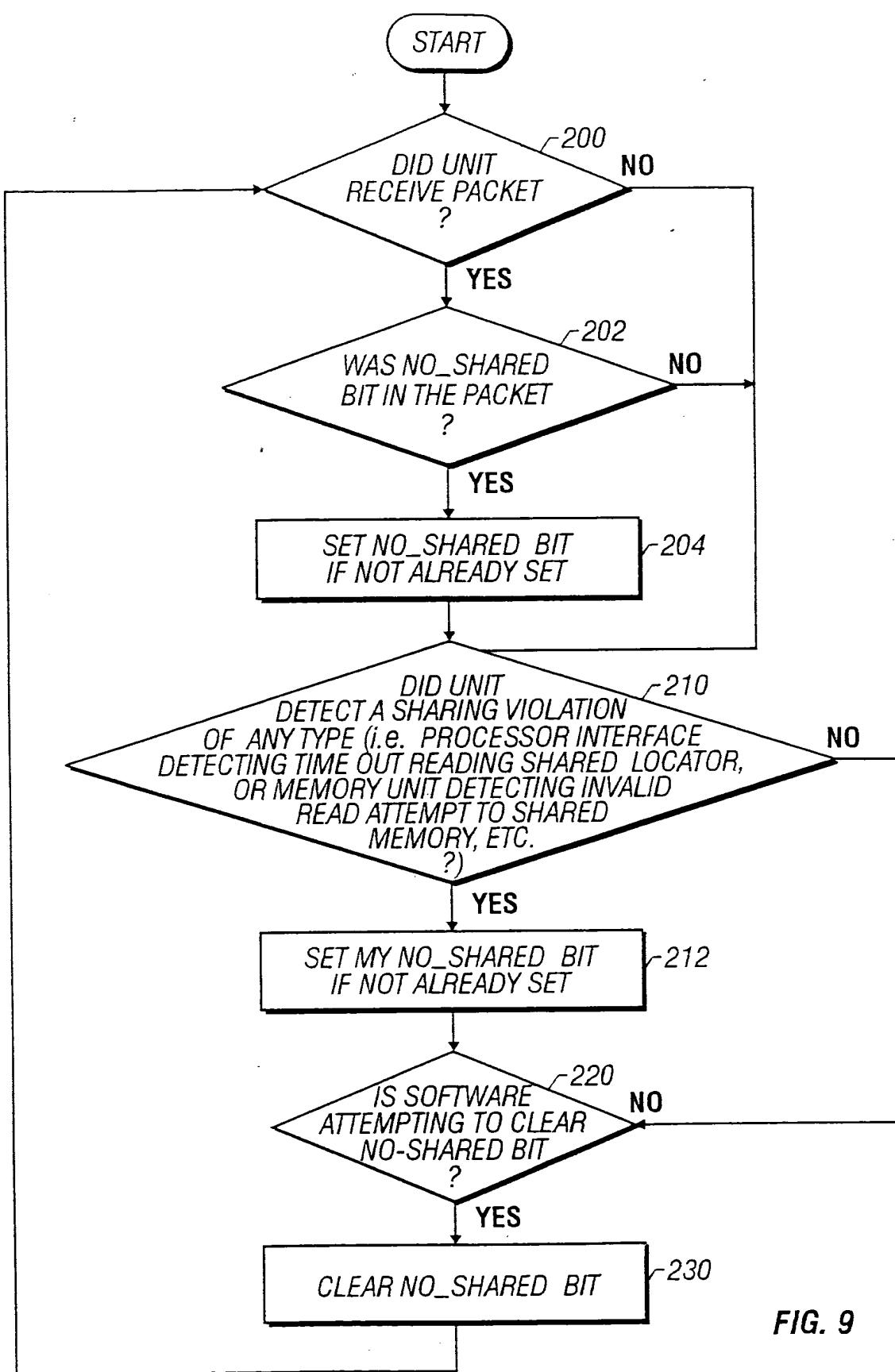


FIG. 9

METHOD FOR ALLOWING DISTRIBUTED HIGH  
PERFORMANCE COHERENT MEMORY WITH FULL ERROR  
CONTAINMENT  
Kent A. Dickey et al.  
10002827-2

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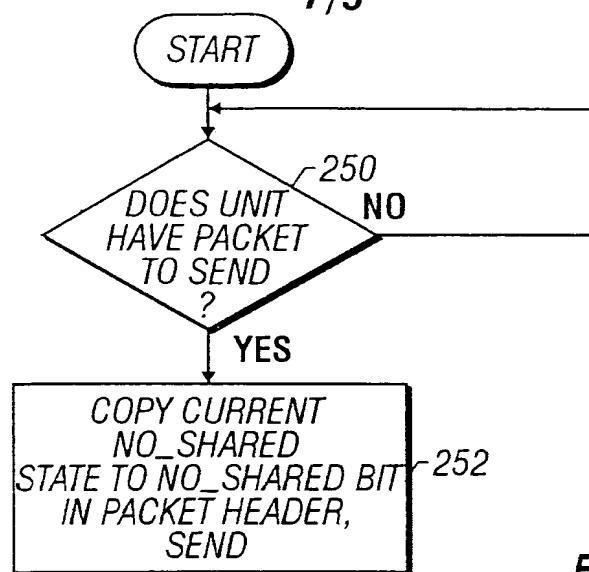


FIG. 10

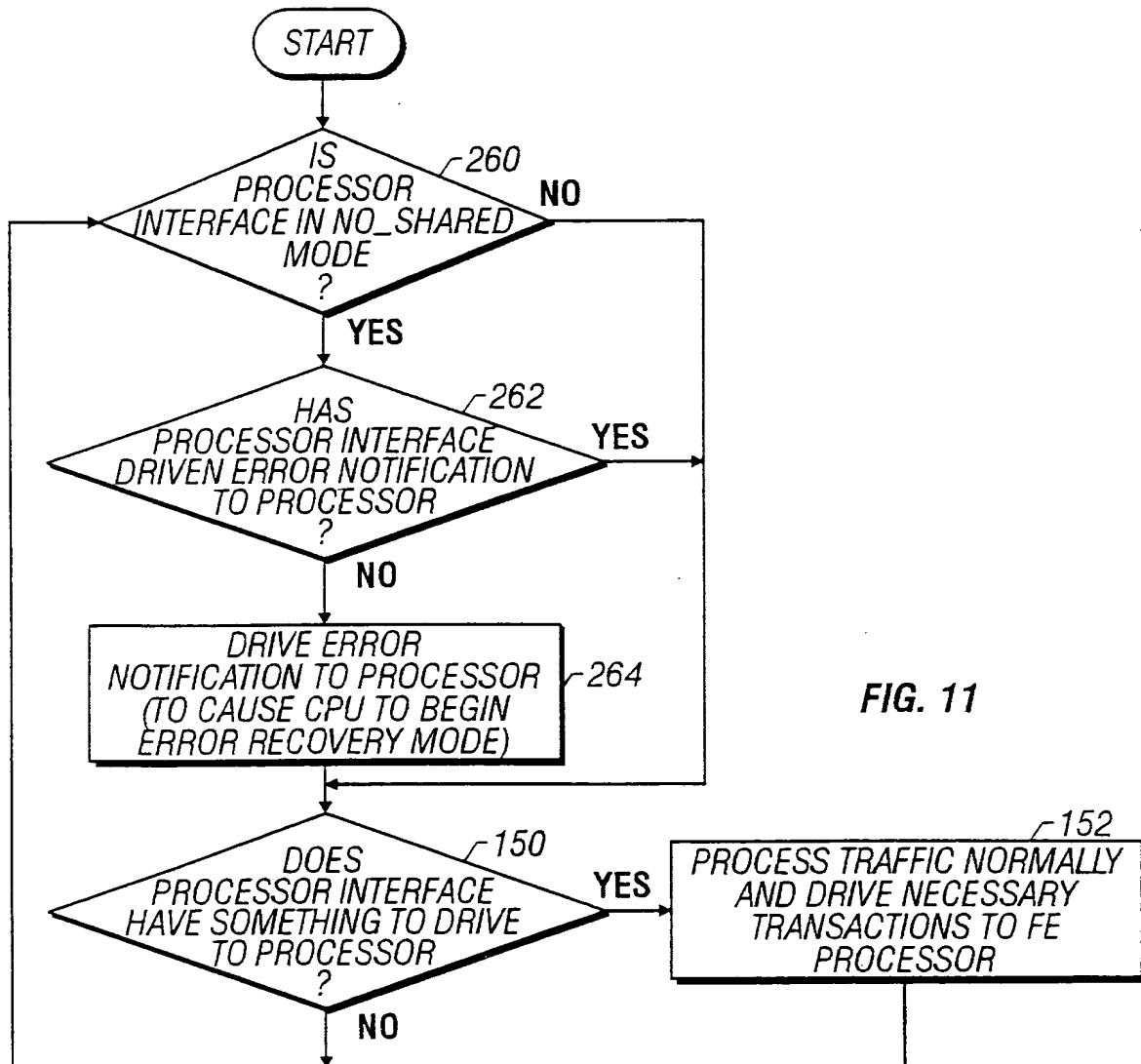


FIG. 11

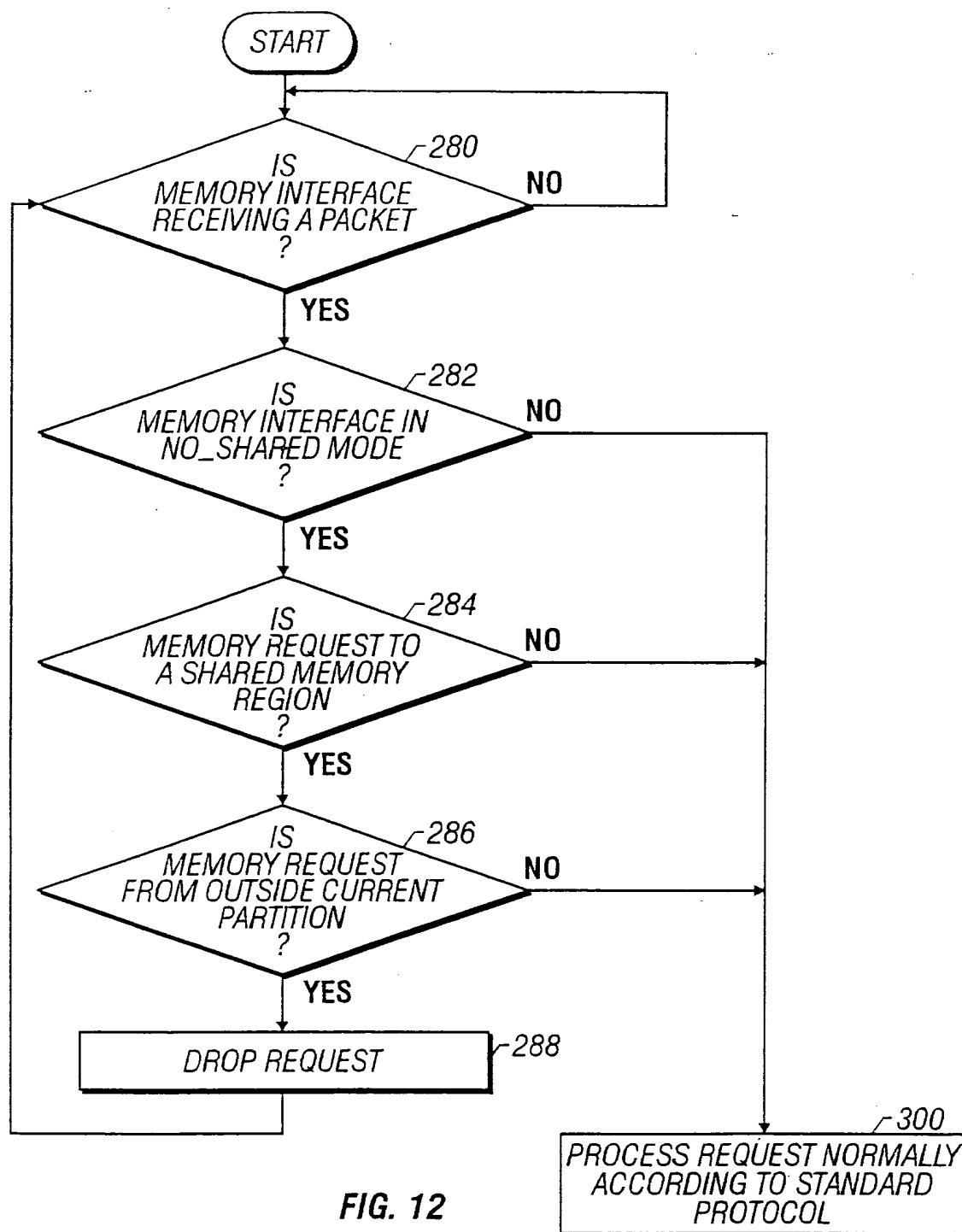


FIG. 12

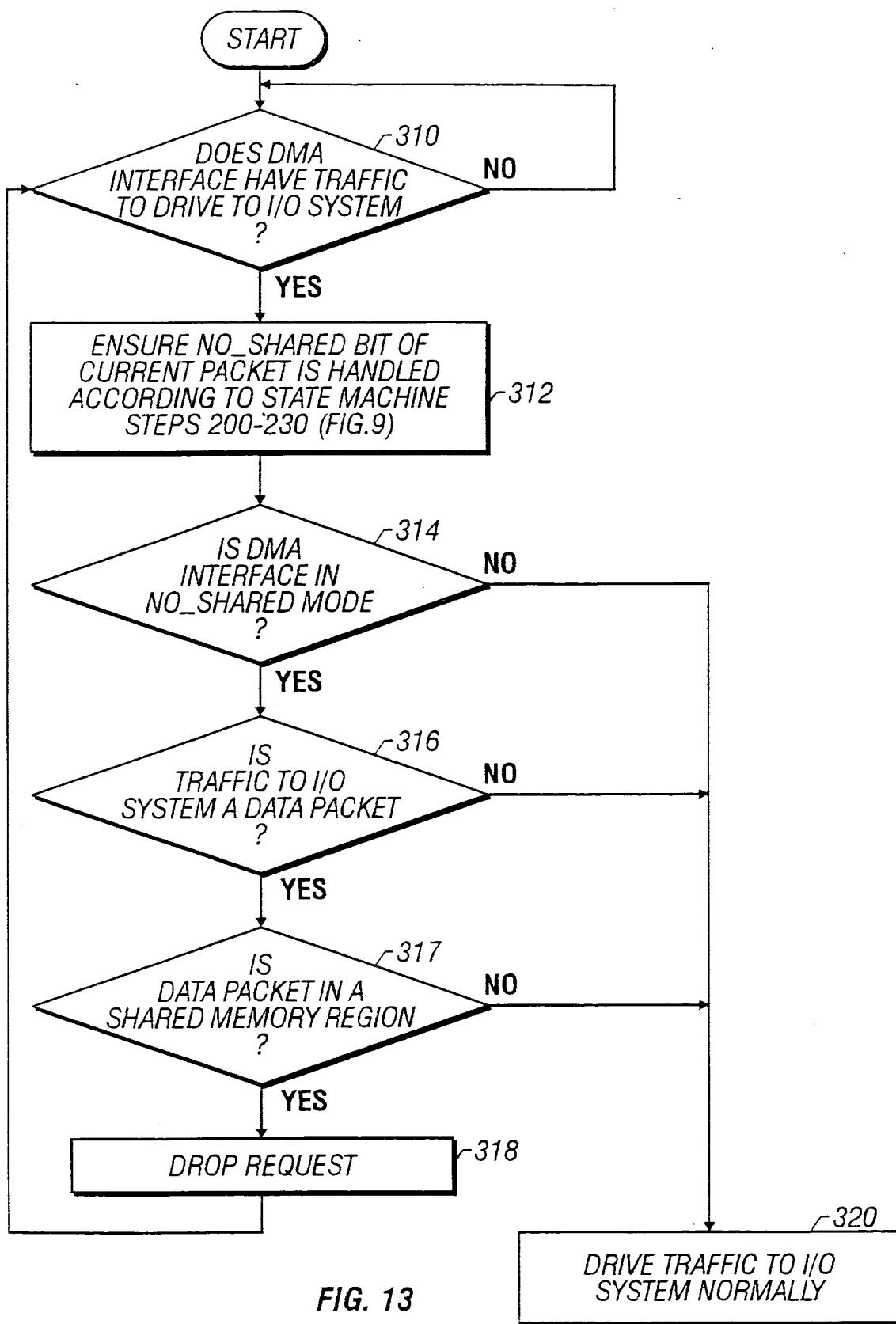


FIG. 13